

R E M A R K S

In the above-identified Office Action, at paragraph 2, various objections were raised as to the form of the claims. In response, Claims 2, 9, 10, 13-15, and 17-20, have been corrected as suggested by the Examiner, and the remainder of the claims which were objected to have been cancelled.

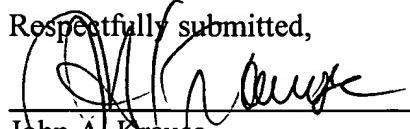
Also, with regard to the objections to the claims as set forth in paragraph 3 of the Office Action, instead of referring to a "common line", independent Claim 2 now specifies the connections between the control terminals of the respective transistors.

In this regard Applicant respectfully submits that the connections which are now specifically called for in the claims, are not disclosed in the prior art. For example, in the cited Takahashi patent the signal output line (Fig. 1, line from 6 to 7) is not connected to the control terminals (i.e., the gate electrodes) of the transfer transistor 4 or the reset transistor 3 in Fig. 1.

Accordingly, it is believed that the claims are all allowable over the cited references. For these various reasons Applicant solicits the issuance of a formal Notice of Allowance.

Applicant's undersigned attorney may be reached in our New York Office by telephone at (212) 218-2100. All correspondence should continue to be directed to our address listed below.

Respectfully submitted,

  
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